



Image

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q56320

Kaoru NARITA

Appln. No.: 09/421,273

Group Art Unit: 2823

Confirmation No.: 3894

Examiner: J. Garcia

Filed: October 20, 1999

For: SEMICONDUCTOR DEVICE HAVING PROTECTION CIRCUIT IMPLEMENTED BY  
BIPOLAR TRANSISTOR FOR DISCHARGING STATIC CHARGE CURRENT AND  
PROCESS OF FABRICATION

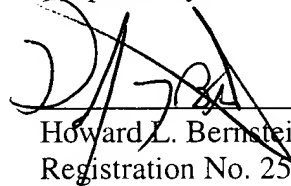
**REQUEST FOR INITIALED FORM PTO-1449**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability (Part of Paper No. 1125), dated December 3, 2003, Applicant respectfully requests that the Examiner initial and return a copy of the Form PTO-1449 filed with the Information Disclosure Statement on December 30, 1999 to the undersigned. For the Examiner's convenience, a copy of the form is attached hereto.

Respectfully submitted,

  
Howard L. Bernstein  
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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: January 5, 2004

